

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S28	2	"5634058".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/18 21:26
S29	2	"20040194059"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 12:29
S30	2	"20020166081"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 12:36
S31	7	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)) same path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:08
S32	2	"20050022155"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:10
S34	19	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:11
S35	8	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm.	US-PGPUB	OR	ON	2009/04/20 13:11

S36	1	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 )	US-PGPUB	OR	ON	2009/04/20 13:13
S37	1	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)). clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 )	US-PGPUB	OR	ON	2009/04/20 13:15
S42	4	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) and (717/123 717/13/ 717/143 717/144717/155 717/156 )	US-PGPUB	OR	ON	2009/04/20 13:16
S44	11	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same path and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB	OR	ON	2009/04/20 13:18
S45	1	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same path.clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB	OR	ON	2009/04/20 13:19

S46	4	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same path.clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:19
S47	13	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4.clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:29
S49	11	((hdl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4 and path).clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:30
S50	2	((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language) and path). clm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:31

S51	7	((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) and path).dm. and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:31
S53	22	((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same path) and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:32
S54	14	((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same path and net \$list) and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:33

S55	14	((hdi) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same path and net \$list) and (717/101 717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:33
S56	16	((hdi) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (design adj file path) and net\$list) and (717/101 717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:34
S57	14	((hdi) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (lc near2 design) and net\$list) and (717/101 717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:34

S58	14	((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (lc near2 design) and net\$list and (717/101 717/123 717/138 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:43
S59	91	((hdl) (vhdl) (hardware adj description adj language) (verilog) ascii) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (lc near2 design) and net\$list (file adj path)) and (717/101 717/123 717/138 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:46
S60	15	((hdl) (vhdl) (hardware adj description adj language) (verilog) ascii) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (lc near2 design) and net\$list (file adj path) and (rapid \$make)) and (717/101 717/123 717/138 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FFRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:51

S61	1	((hndl) (vhdl) (hardware adj description adj language) (verilog) ascii) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (lc near2 design) and net\$list (file adj path) and (rapid \$make)) and (717/101 717/123 717/138 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB	OR	ON	2009/04/20 13:51
S63	9	((hndl) (vhdl) (hardware adj description adj language) (verilog) ascii) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (lc near2 design) and net\$list (file adj path)).clm. and (717/101 717/123 717/138 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:56
S64	33	path same ((hndl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4 and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:36

S65	51	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:36
S67	14	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and index and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:37
S69	40	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and (integrated adj circuit) and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:38
S70	6	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 same (integrated adj circuit) and (717/123 717/13/ 717/143 717/144717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:44

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